ABOUT THE COLLEGE

K.L.N. College of Engineering (KLNCE), established in 1994 near Madurai, Tamil Nadu, is the first self-financing co-educational engineering college in the region. Founded by philanthropist Thiru K.L.N. Krishnan, this autonomous institution is approved by AICTE, affiliated with Anna University, and accredited by NAAC and NBA. Spanning a 53.8-acre campus, KLNCE offers diverse undergraduate, postgraduate, and doctoral programs across fields like Mechanical, Computer Science, EEE, ECE, AI & DS, and Management. Equipped with modern laboratories, research centers, a central library, and active student clubs, the college focuses on holistic technical education, innovation, and industry alignment.

ABOUT THE ECE DEPARTMENT

Established in 1994, the Department of Electronics and Communication Engineering (ECE) is one of the foundational and largest academic departments at K.L.N. College of Engineering. NBA-accredited and affiliated with Anna University, it offers undergraduate (B.E.), postgraduate (M.E. in Communication Systems), and Ph.D. research programs. The department focuses on key areas like VLSI technology, signal processing, embedded systems, RF communication, and optical networks. Equipped with state-of-the-art laboratories, industry-standard design software, and experienced faculty, the department prepares students for modern industry demands through experiential learning, research initiatives, technical clubs, and strong placement training in core electronics and IT sectors.

VISION OF THE DEPARTMENT

To Promote as a centre of excellence in educational and research activities related to Electronics and Communication Engineering and its allied areas.

MISSION OF THE DEPARTMET

- To create educational and research environment to meet ever changing and ever demanding needs of Electronics and Communication Industry along with IT and other interdisciplinary fields.
- To mould the students to become ethically upright and recognized as responsible engineers.

CONTACT FOR FURTHER DETAILS

COORDINATOR

Dr.S.SUBHA M.E., M.B.A., Ph.D.,
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AICTE Training and Learning (ATAL) Academy

Sponsored

ONLINE

FACULTY DEVELOPMENT PROGRAM

titled on

NEXT – GENERATION SEMICONDUCTOR ECO-SYSTEMS: DRIVING AI, WIRELESS COMMUNICATIONS, AUTOMOTIVE AND HEALTHCARE INNOVATIONS

23rd to 28th November 2026

6.00 P.M. to 9.00 P.M.

Organized by

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

K.L.N. COLLEGE OF ENGINEERING

Pottapalayam, Sivaganga District – 630 612

Chief Patron
Dr. A.V. RAMPRASAD
Principal

Convener
Dr. V.KEJALAKSHMI M.E., M.B.A., Ph.D.,
Professor & Head
Department of Electronics and Communication Engineering

ABOUT THE FACULTY DEVELOPMENT PROGRAM

The "Next-Generation Semiconductor Ecosystems" Faculty Development Program (FDP) bridges higher education with rapid global microelectronics advancements beyond traditional silicon scaling. The program centers on four key application pillars: AI (HPC chips, chiplets, Edge AI), wireless communications (6G testbeds, mmWave, RFIC design), automotive electronics (SiC/GaN power semiconductors, ADAS radar), and smart healthcare (lab-on-a-chip, bio-microfluidics). Combining expert lectures with hands-on EDA tool workshops, the initiative covers advanced fabrication, packaging, and System-on-Chip (SoC) co-design. Ultimately, the FDP empowers educators to modernize engineering curricula, advance departmental research, foster industry-academia partnerships, and train the next generation of VLSI engineers.

KEY THRUST AREAS

Modern Semiconductor Devices, FINFET, GaFET,
AI – Driven EDA Tools Hardware and Accelerators
5G – Advanced / 6G Wireless Semiconductors
Autonomous Driving, Sensor Fusion and LiDAR
Smart Health Care Bio Sensors and Bio-medical SoCs

OBJECTIVE OF THE FDP

This Faculty Development Program (FDP) aims to build advanced technical competencies, research capabilities, and pedagogical skills among educators in modern semiconductor ecosystems. It deepens understanding of emerging silicon technologies, heterogeneous packaging, and novel materials like SiC and GaN, while exploring domain-specific designs for AI, 6G, automotive, and healthcare applications. Through hands-on EDA tool training and direct engagement with industry experts, the initiative bridges the academic-industry skill gap. Ultimately, the FDP guides faculty to modernize VLSI curricula, establish multidisciplinary lab experiments, and foster collaborative research, positioning academic institutions as active drivers of global microelectronics growth.

GUIDELINES FOR PARTICIPANTS

- Eligibility: Faculty members, Research Scholars & PG Scholar of the AICTE approved Institutions and Industry Personnel
- No registration fees: Sponsored by AICTE ATAL Academy
- Participants must register on the official AICTE ATAL portal , refer the link https://atalacademy.aicte.gov.in/assets/importantDocument/ATAL/2026-27/online/Participant_Consent_Letter.pdf
- The certificates shall be issued to those participants who are registered on ATAL portal https://www.aicte.gov.in/atal and attend the program with minimum 80% attendance and score minimum 70% marks in the test.

EMINENT RESOURCE PERSONS

INDUSTRY EXPERTS

Mr. Sivanandh Ramadass
Principal Engineer, Tech Lead, META, California

Mr. Anent Prakash
Senior Manager, Synopsys Inc, Austin, Texas

Mr. Sivasakthi kumar Balaji T M
Senior Director – Engineering
ACL Digital, Bangaluru, Karnataka

Dr. Kushal Shah
Associate Director – Products (AI&ML)
Dharma.h Software Technologies, Bhopal, Madhya Pradesh

Mr. Anish Ahammed M
Senior Staff Wireless Power Consortium
Dolby Laboratories, Bengaluru, Karnataka

Mr. Anandh Babu G S
Senior Engineering Manager
Broadcom, Bengaluru, Karnataka

Dr. Arun Divakaran
Co-Founder, Executive Corporate Trainer,
AI Strategist, Chennai, TamilNadu

Mr. Maheswaran Samuthiram
Director, Research & Development
Aptean Company, Madurai

ACADEMIC EXPERTS

Dr. Jebasini Ponnian
Head of Post Graduate Program – Electrical and Electronics Engineering
Kuala Lumpur University of Science & Technology, Malaysia

Dr. Rajaram S
Registrar & Professor, ECE
Thiagarajar College of Engineering, Madurai

Dr. Sivasankar P
Professor & Head, Department of Electrical and Electronics Engineering
NITTTR Chennai

Dr. C. Vennila
Principal & Professor, ECE
V.S.B Engineering College, Karur

Dr. Surya Teja Naga Srinivas Padala
Assistant Professor
National Institute of Technology (NIT), Trichy

ATAL AICTE FACULTY DEVELOPMENT PROGRAMME

Schedule - Online 2026

K.L.N. COLLEGE OF ENGINEERING

(An Autonomous Institution, Affiliated to Anna University Chennai)

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Title: Next – Generation Semiconductor Eco-systems: Driving AI, Wireless Communications, Automotive and Healthcare Innovations **Date: 23rd to 28th Nov 2026**

DAY 1 (23.11.2026)	DAY 2 (24.11.2026)	DAY 3 (25.11.2026)	DAY 4 (26.11.2026)	DAY 5 (27.11.2026)	DAY 6 (28.11.2026)
6.00 - 6.30 P.M.	6.00 - 7.30 P.M.	6.00 - 7.30 P.M.	6.00 - 7.30 P.M.	6.00 - 7.30 P.M.	2.00 - 3.30 P.M.
INAUGURAL SESSION	**Mr. Sivanandh Ramadass** Principal Engineer, Tech Lead, META, California Experience: 25 years Topic: Mapping AI/Deep-Learning Models onto Edge and Semiconductor Accelerator Platforms	**Mr. Sivasakthi kumar Balaji T M** Senior Director – Engineering ACL Digital, Bengaluru, Karnataka Experience: 25 years Topic: EVs, ADAS, Autonomous Driving, Radar, LiDAR, and Sensor Fusion	**Dr. Jebasini Ponnian** Head of Post Graduate Program – Electrical and Electronics Engineering, Kuala Lumpur University of Science & Technology, Malaysia Experience: 22 years Topic: AI-Assisted IC Design, Simulation, Synthesis, Physical Design, and Performance Analysis	**Dr. Surya Teja Naga Srinivas Padala** Assistant Professor, National Institute of Technology, Trichy Experience: 11 years Topic: Semiconductor Technology Roadmap: FinFET, GAAFET, Chiplets, 2.5D/3D ICs, and Heterogeneous Integration	**Dr. Rajaram S** Registrar & Professor, ECE Thiagarajar College of Engineering Madurai Experience: 29 years Topic: Modern Semiconductor Devices and Materials
6.30 - 8.00 P.M.	7.30 – 9.00 P.M.	7.30 – 9.00 P.M.	7.30 – 9.00 P.M.	7.30 – 9.00 P.M.	3.30 – 5.00 P.M.
Dr. Sivasankar P Professor & Head, Department of Electrical and Electronic Engineering, NITTTR Chennai Experience: 24 years Topic: Semiconductor Technologies for 5G-Advanced / 6G Communications	**Mr. Anent Prakash** Senior Manager, Synopsys Inc, Austin, Texas Experience: 25 years Topic: AI assisted semiconductor design to manufacturing flow	**Mr. Anish Ahammed M** Senior Staff Wireless Power Consortium, Dolby Laboratories Inc., Bengaluru, Karnataka Experience: 23 years Topic: AI-Driven Electronic Design Automation & Beyond Conventional AI Hardware	**Dr. Kushal Shah** Associate Director – Products (AI&ML), Dharma.h Software Technologies, Bhopal. Madhya Pradesh Experience: 21 years Topic: From Alphafold to ESM3: The Era of Programmable Biology	**Dr. Arun Divakaran** Co-Founder Executive Corporate Trainer, AI Strategist, Chennai, TamilNadu Experience: 20 Years Topic: AI – Enhanced Engineering Pedagogy	**Mr. Maheswaran Samuthiram** Director Research & Development, Aptean Company, Madurai. Experience: 22 Years Topic: The Agent Hour Economy: Scaling AI-Native Workflows in Enterprise systems
8.00 – 9.30 P.M.					5.00 – 6.30 P.M.
Dr. C.Vennila Principal & Professor, ECE V.S.B Engineering College, Karur Experience: 32 years Topic: Feature Semiconductor Frontiers: Silicon Photonics, Quantum Devices, 2D materials and Emerging Computing Paradigms					Dr. S.Suganya MBBS., MD(Psy)., Consultant Psychiatrist Hannah Joseph Hospital, Madurai Experience: 9 years Topic: Drug use prevention and Early intervention in Higher Educational Institutions
					6.30 to 7.30 P.M. Online Test & Feedback **7.30 to 8.00 P.M.** Valedictory Session